GPIO(PMOD X 4)
GPIO (16X2)

AUDIO

AD/DA

CAMERA

POWER

7-SEG DISPLAY x 6

HDMI

BUTTON X7

SWITCH x 8

LED X 8

XILINX

XC7A100T-2FGG676I

USB PHY x2

I2C EEPROM

TFT LCD

USB 2 UART

USB (MS & KBD)

SRAM (256K X2)

ETHERNET(1G)

FPGA & RISCV JTAG

OSC_50M, 32.768K

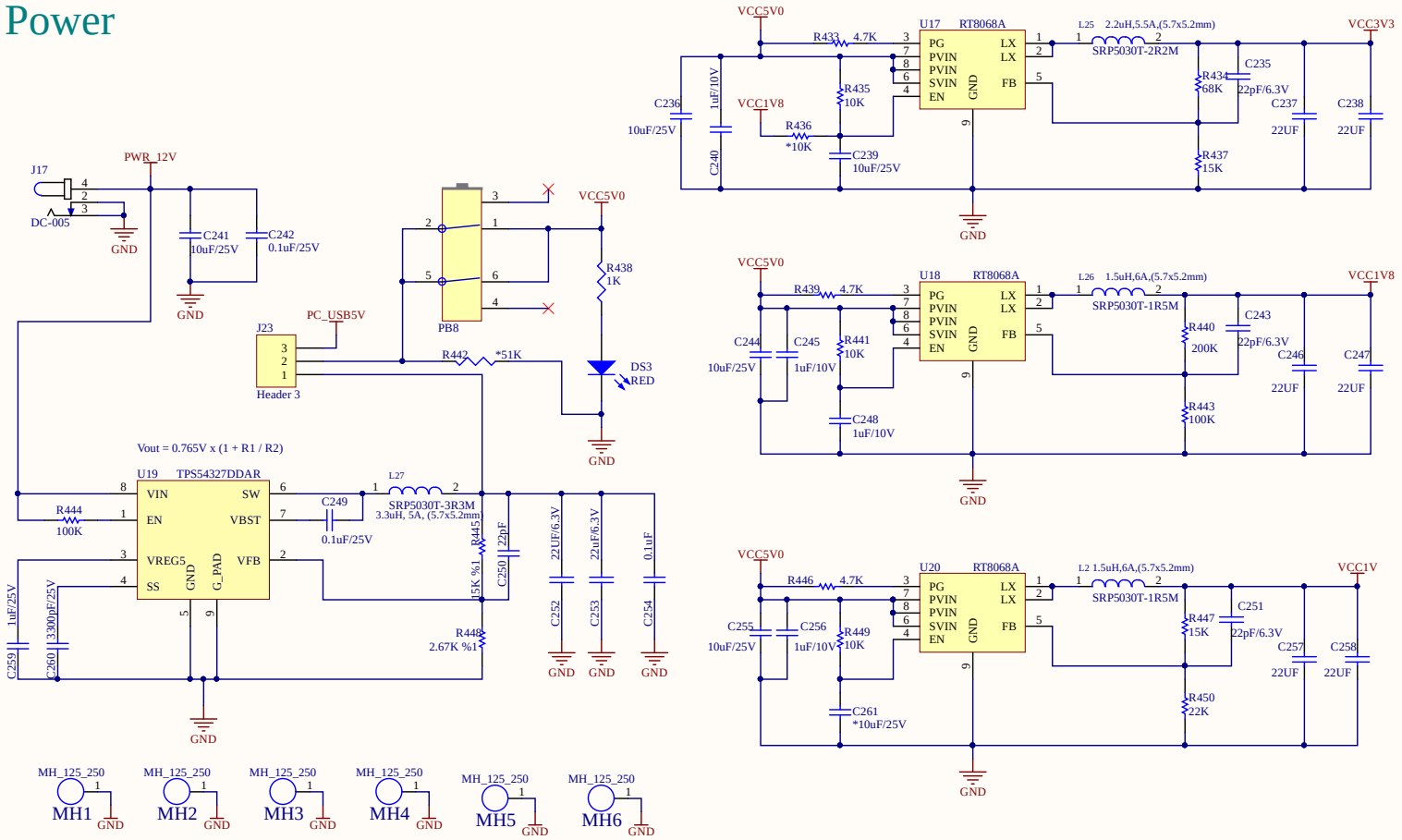
FLASH

IIC Addressing

8'B1001000X	PCF8591T
8'B0111001X	HDMI
8'B1010100X	IIC EEPROM
8'B0011010X	WM8978

Company: FII	Title: Diagram of PRX100T		
Date: 2019-07-20 Time 3:33:03 PM		Engineer: RJ WG	Size: A4
Revision: V3	Sheet 01 of 23		

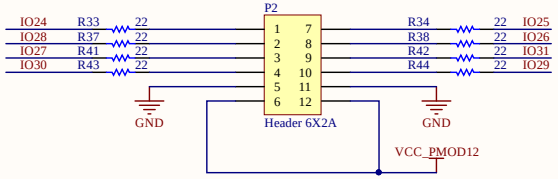
Power

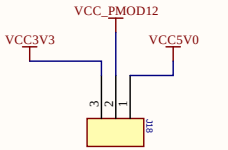


Company: FII	Title: Power	of PRX100T	
Date: 2019-07-20	Time 3:33:03 PM	Engineer: RJ WG	Size: B
Revision: V3	Sheet 02 of 23		

GPIO

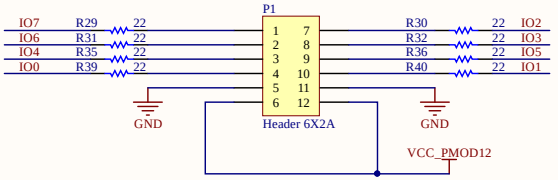
GPIO1



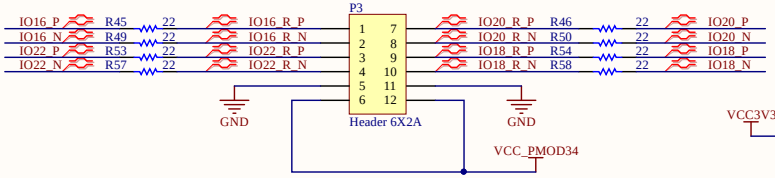


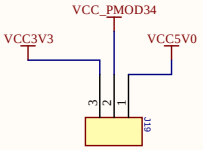
(2.54mm) 1X3PIN MALE

GPIO2



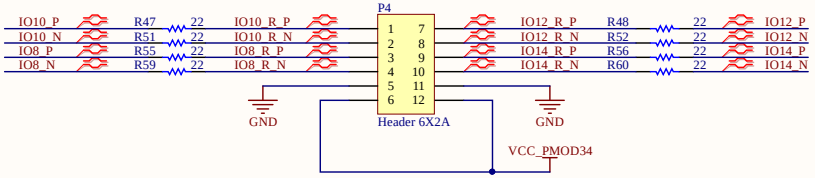
GPIO3



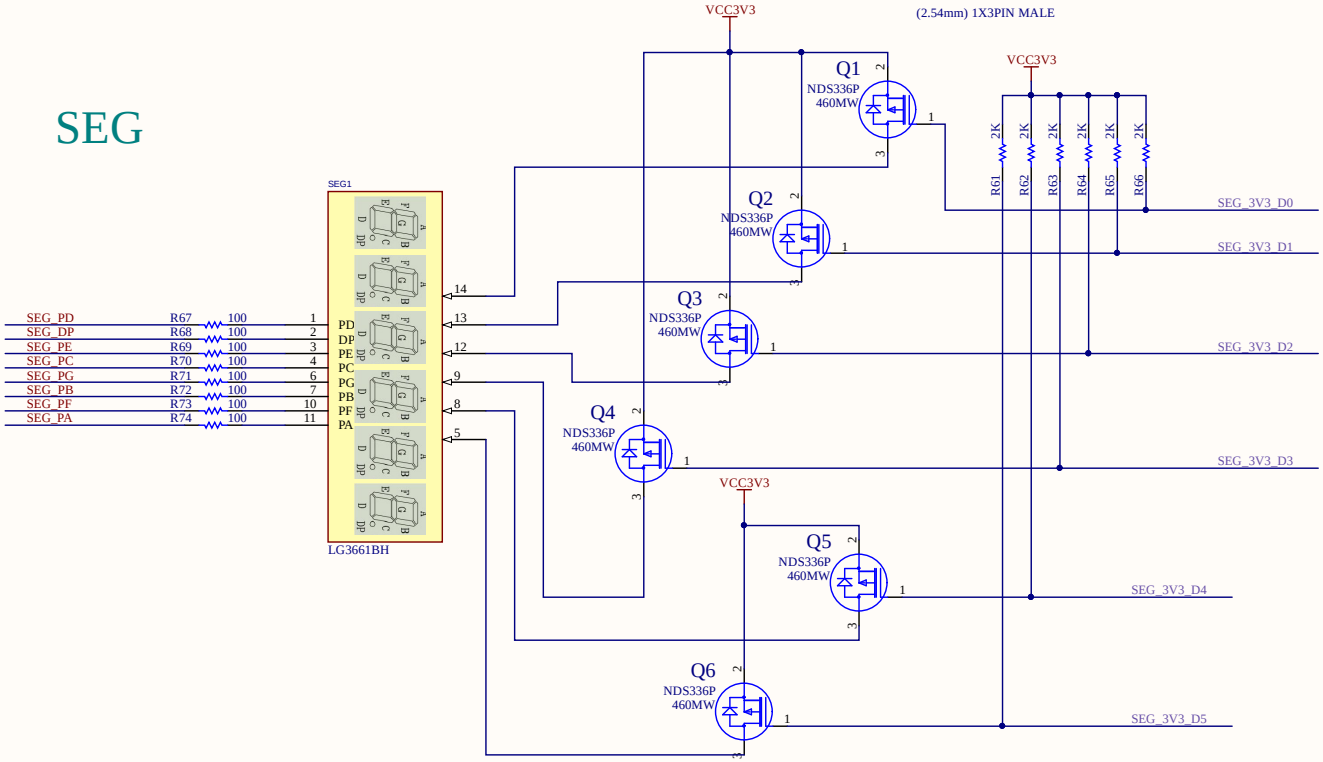


(2.54mm) 1X3PIN MALE

GPIO4

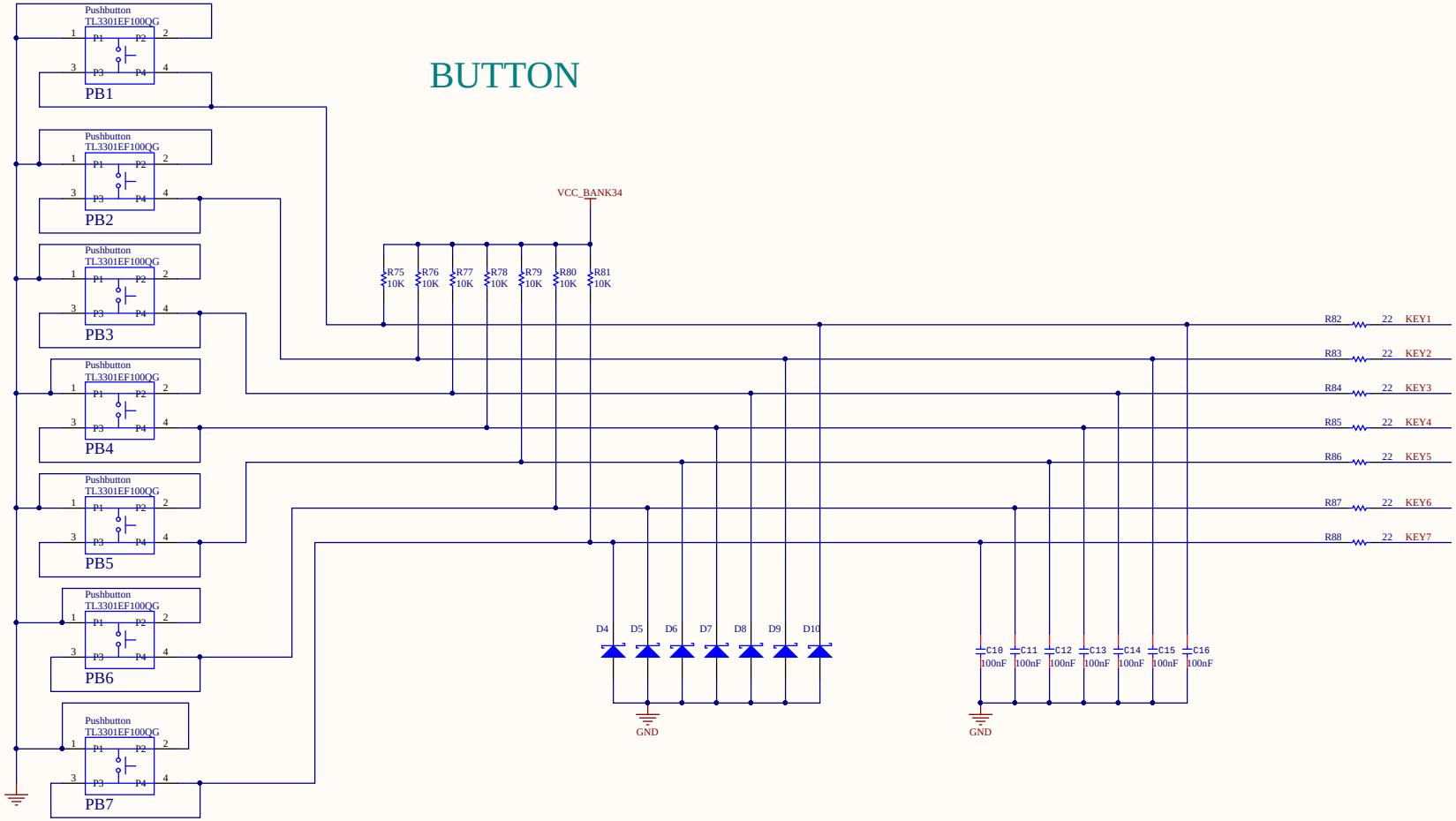


SEG

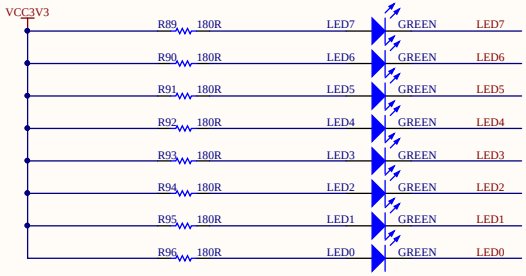


Company: FII	Title: SEG_GPIO	of PRX100T	
Date: 2019-07-20	Time 3:33:04 PM	Engineer: RJ WG	Size: B
Revision: V3	Sheet 03 of 23		

BUTTON

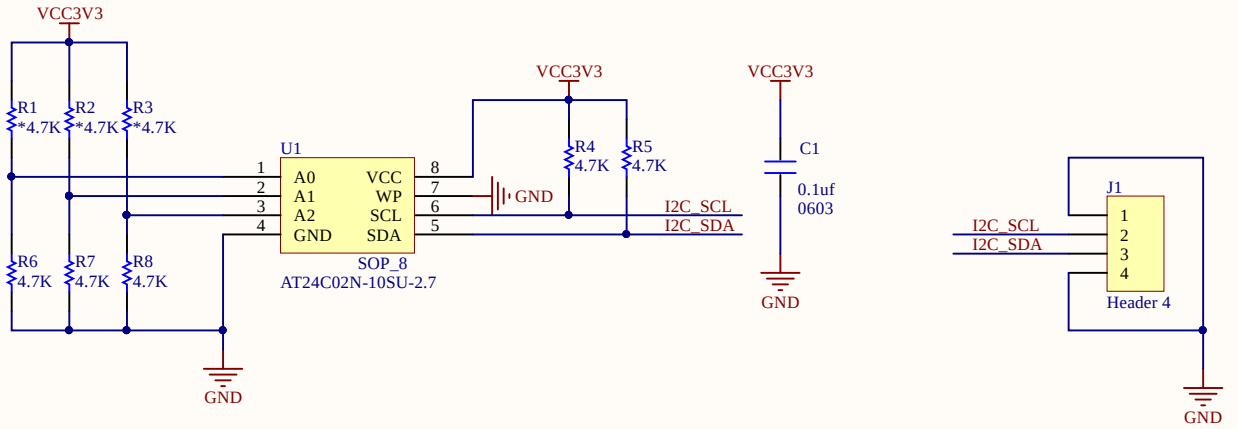


LED

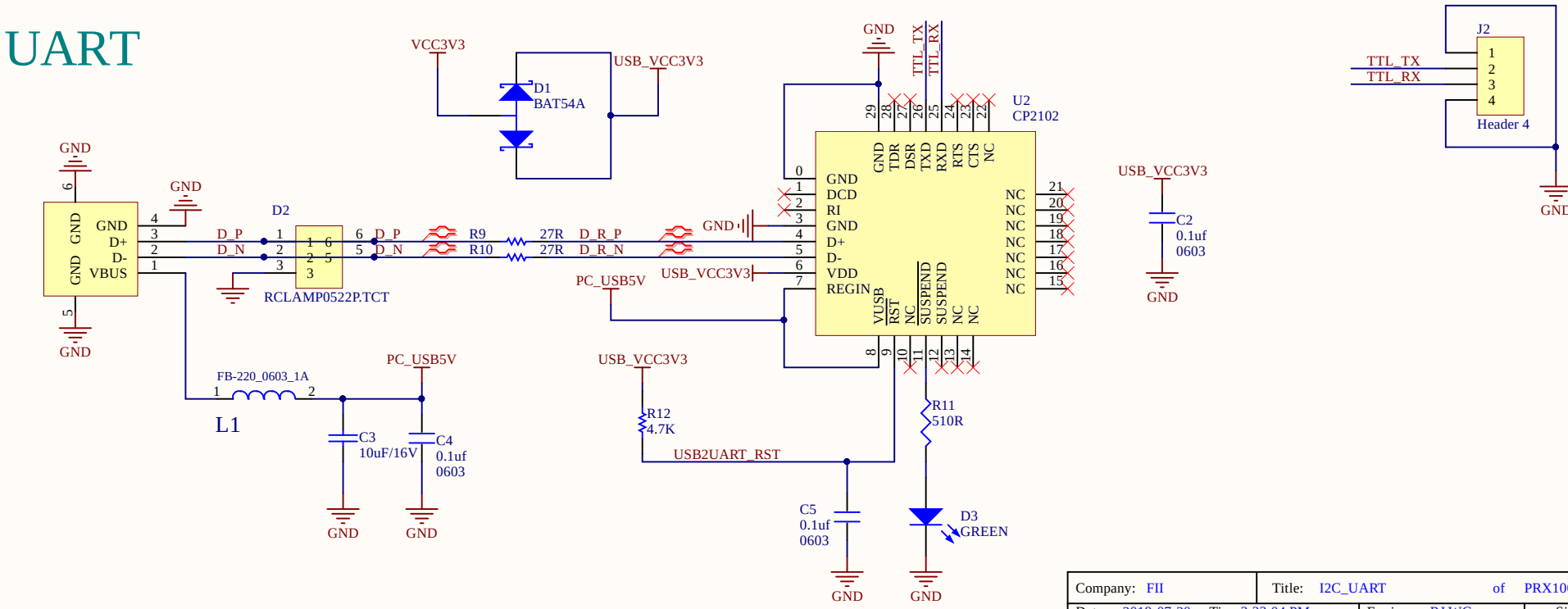


Company: FII	Title: Button_Led of PRX100T		
Date: 2019-07-20 Time 3:33:04 PM		Engineer: RJ WG	Size:
Revision: V3	Sheet 04 of 23		B+

I2C

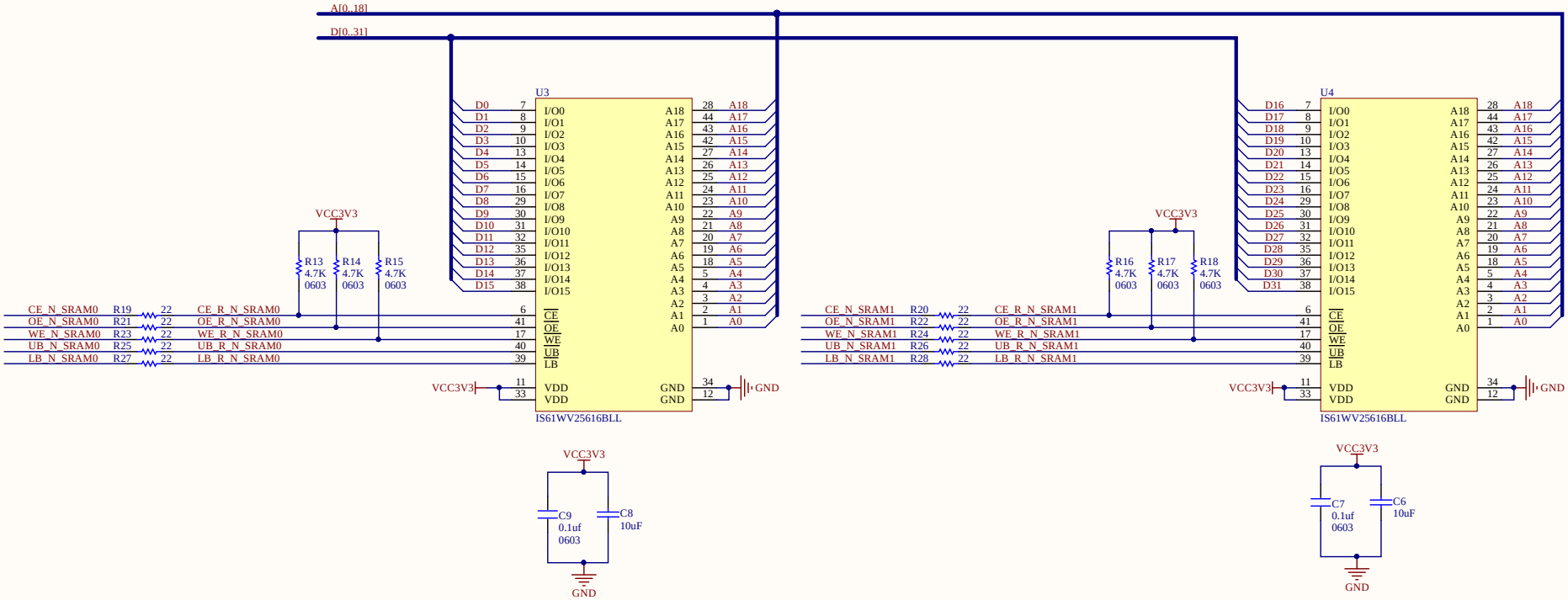


UART



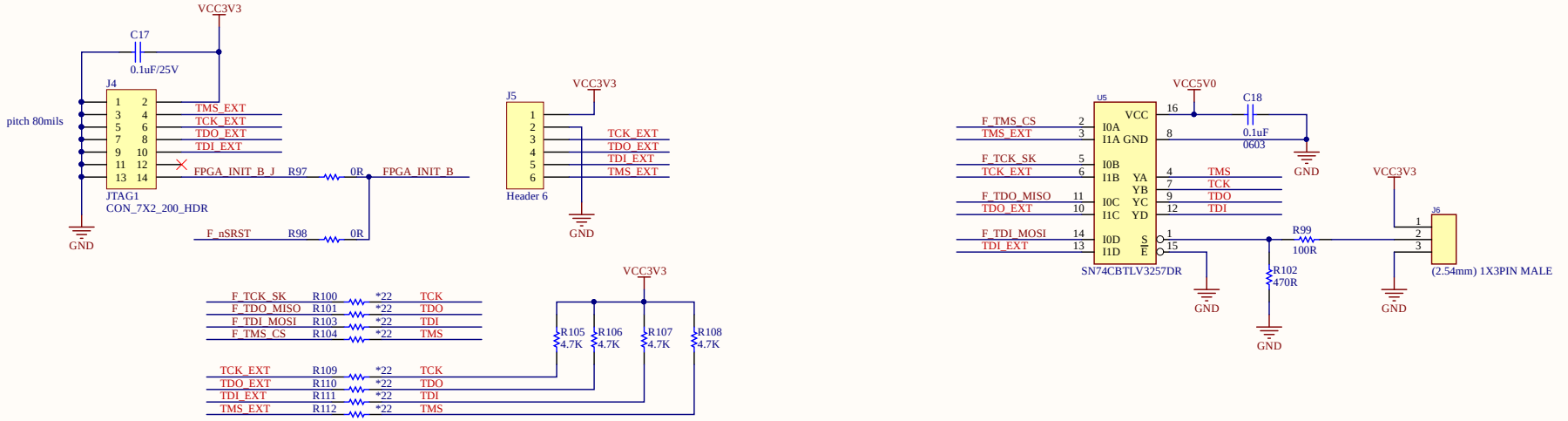
Company: FII	Title: I2C_UART	of PRX100T	
Date: 2019-07-20	Time 3:33:04 PM	Engineer: RJ WG	Size: A4
Revision: V3	Sheet 05 of 23		

SRAM

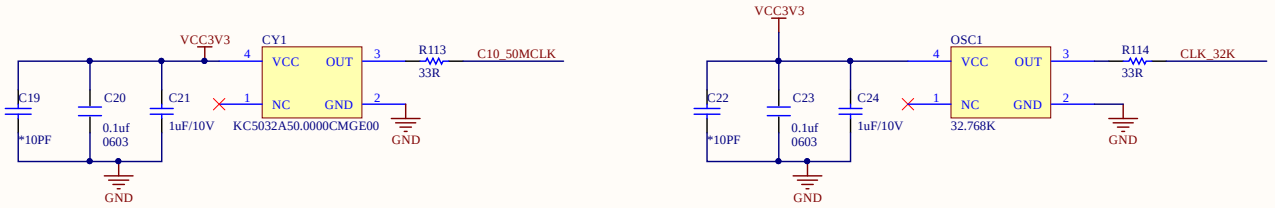


Company: FII	Title: SRAM of PRX100T		
Date: 2019-07-20	Time 3:33:04 PM	Engineer: RJ WG	Size: B
Revision: V3	Sheet 06 of 23		

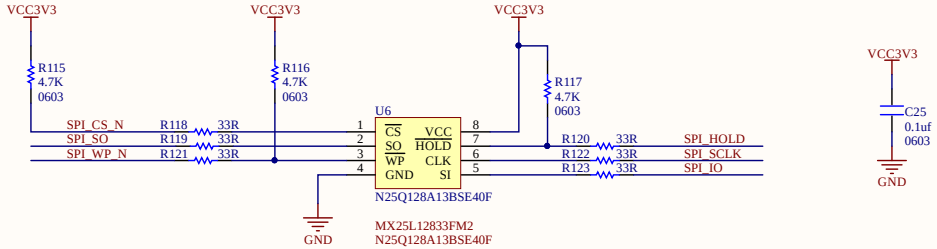
JTAG



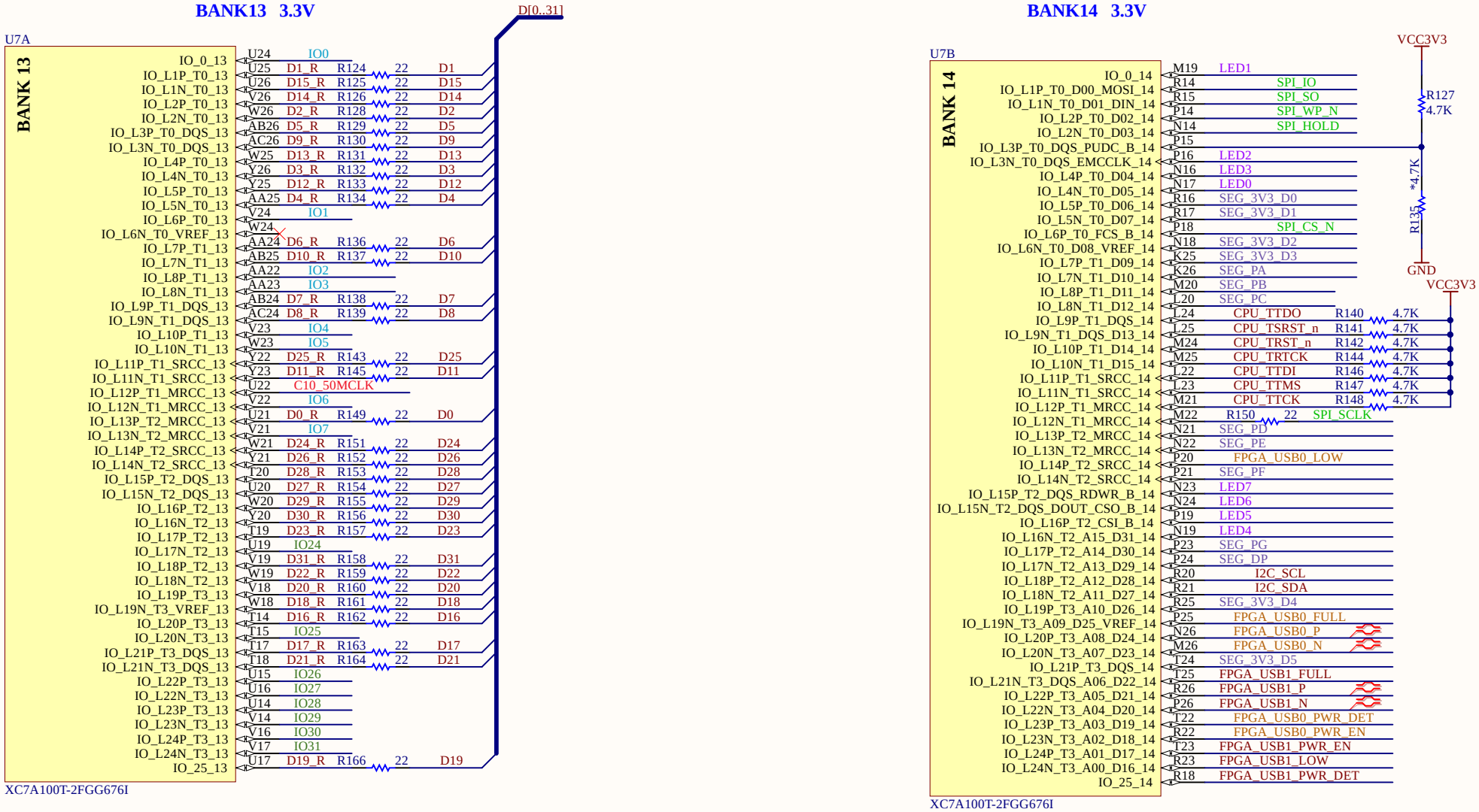
XTAL

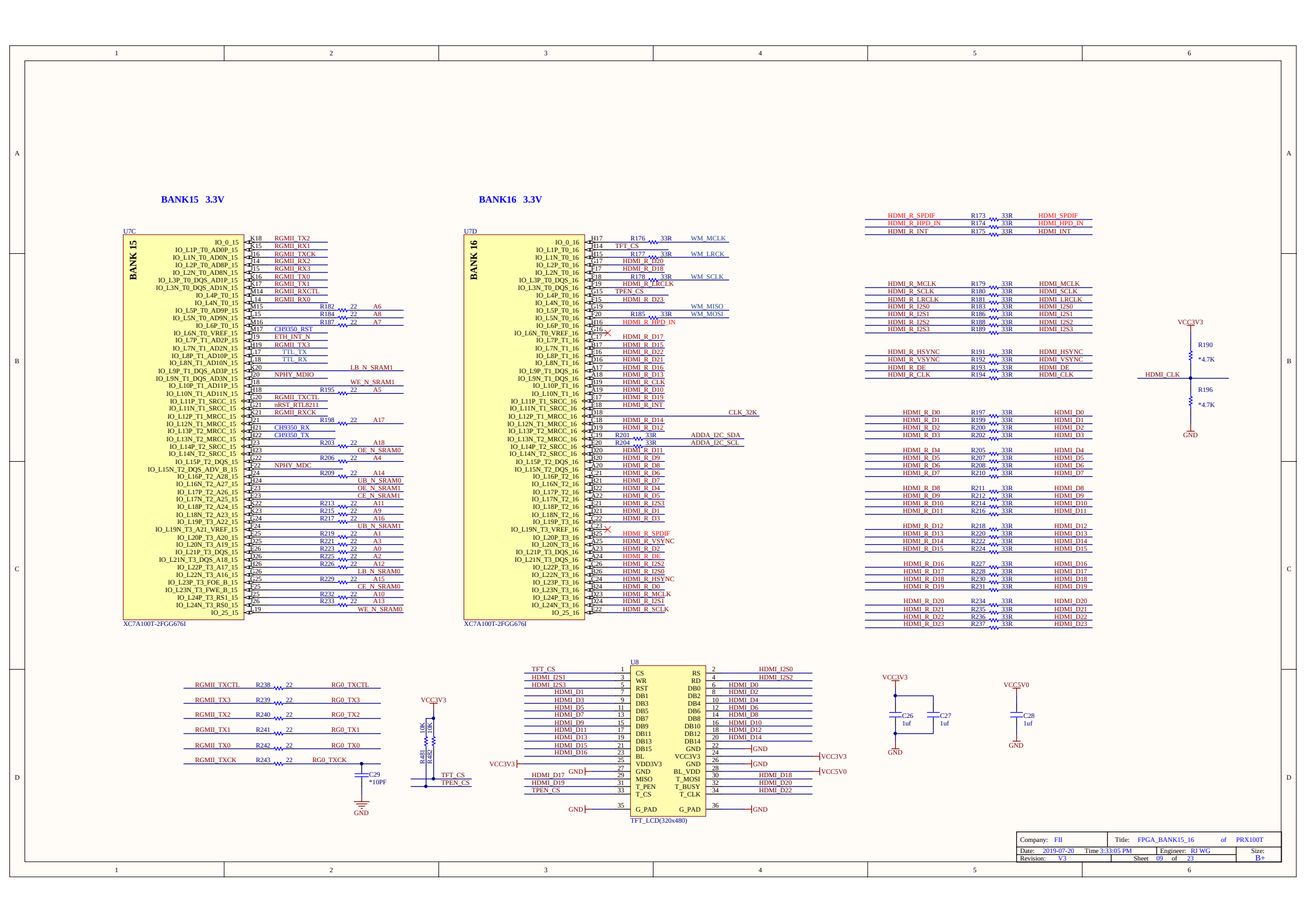


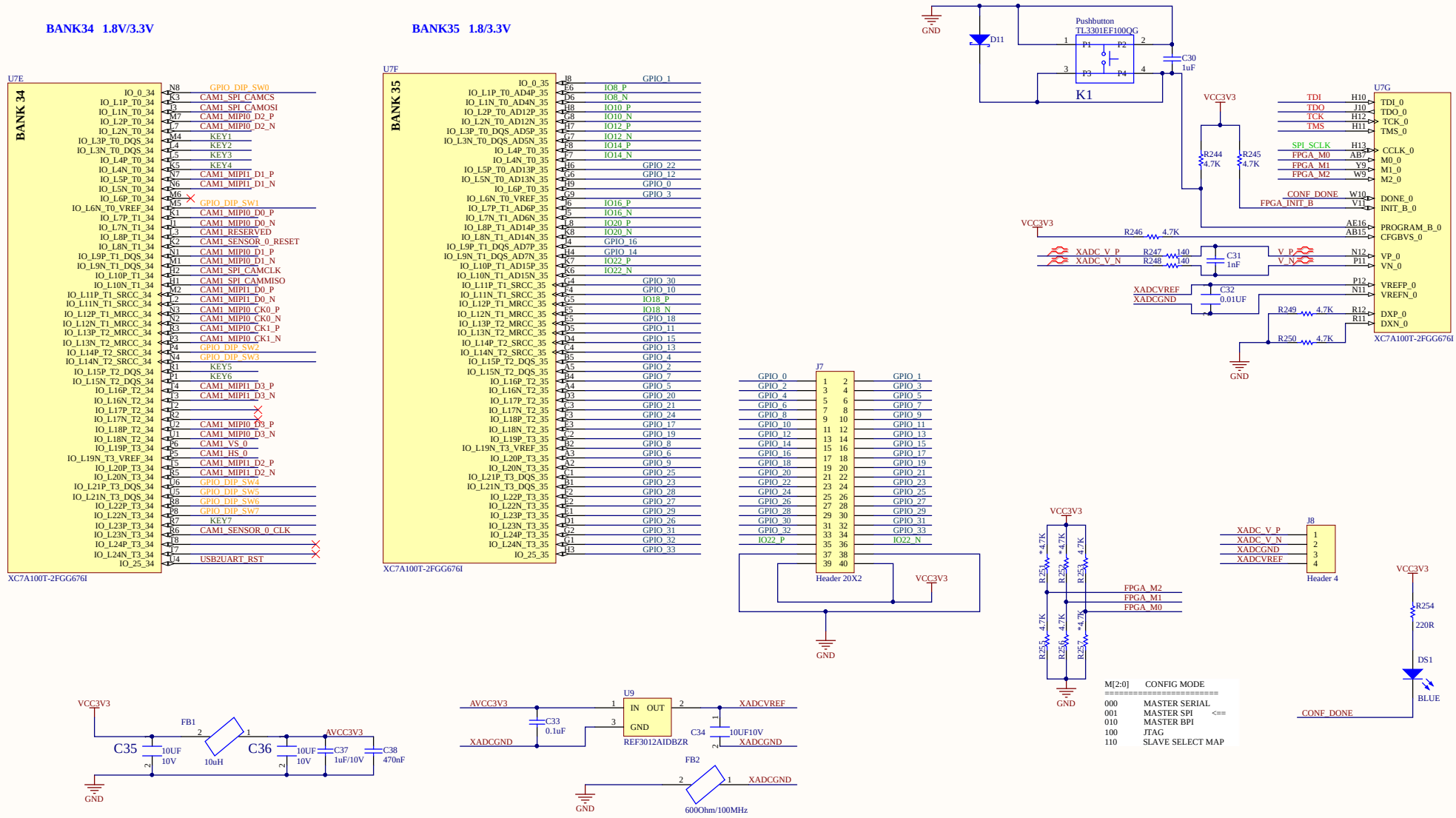
FLASH



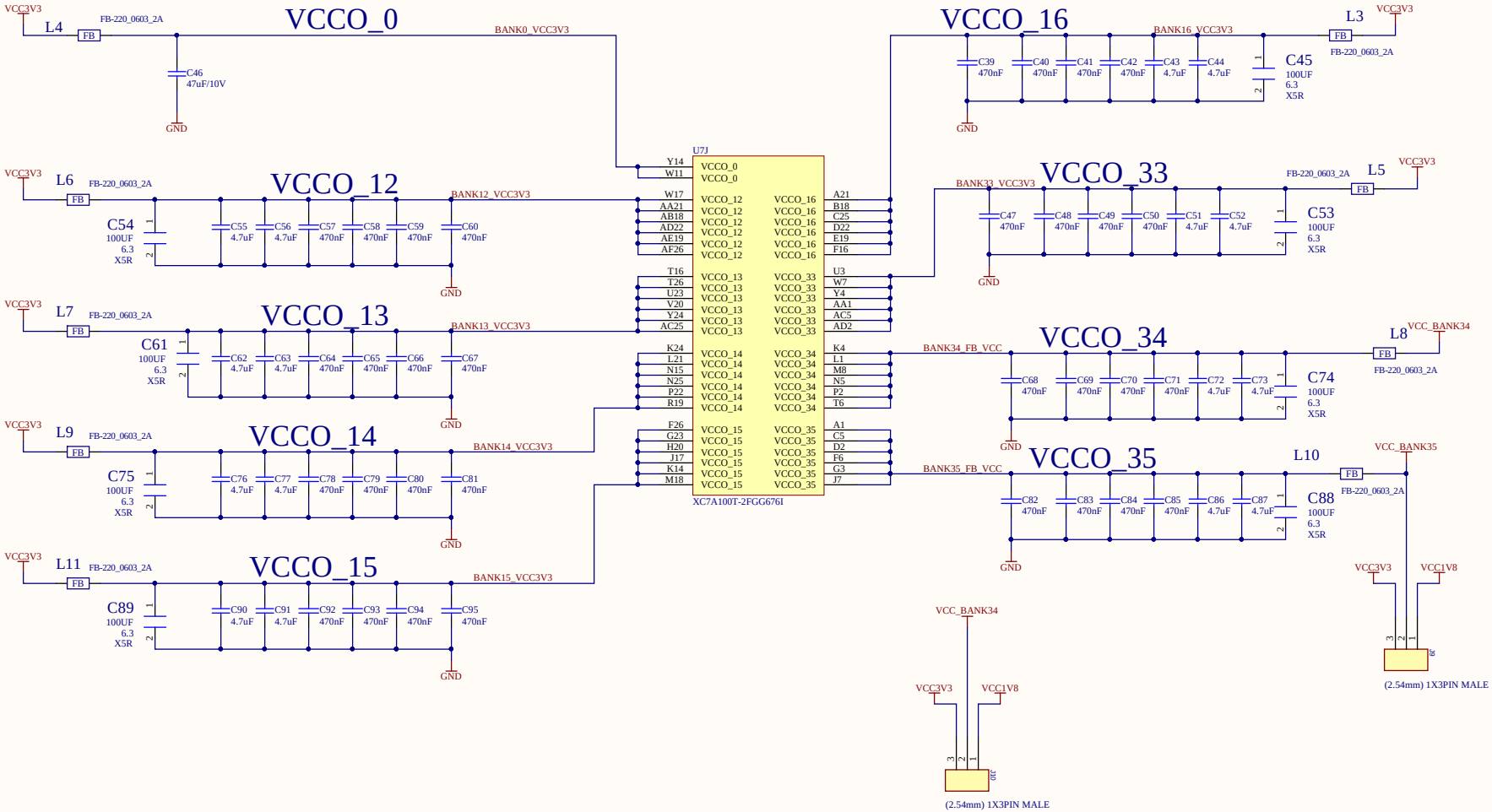
Company: FII	Title: JTAG_OSC_FLASH of PRX100T		
Date: 2019-07-20 Time 3:33:04 PM	Engineer: RJ WG	Size: B	
Revision: V3	Sheet 07 of 23		


BANK13 3.3V
D[0..31]
U7A
BANK 13
XC7A100T-2FGG676I
BANK14 3.3V
U7B
BANK 14
XC7A100T-2FGG676I
VCC3V3
R127
4.7K
*4.7K
R135
GND
VCC3V3
R140 4.7K
R141 4.7K
R142 4.7K
R144 4.7K
R146 4.7K
R147 4.7K
R148 4.7K
R150 22 SPI_SCLK
Company: FII
Title: FPGA_BANK_13_14 of PRX100T
Date: 2019-07-20 Time 3:33:04 PM
Engineer: RJ WG
Revision: V3
Sheet 08 of 23
Size: A4

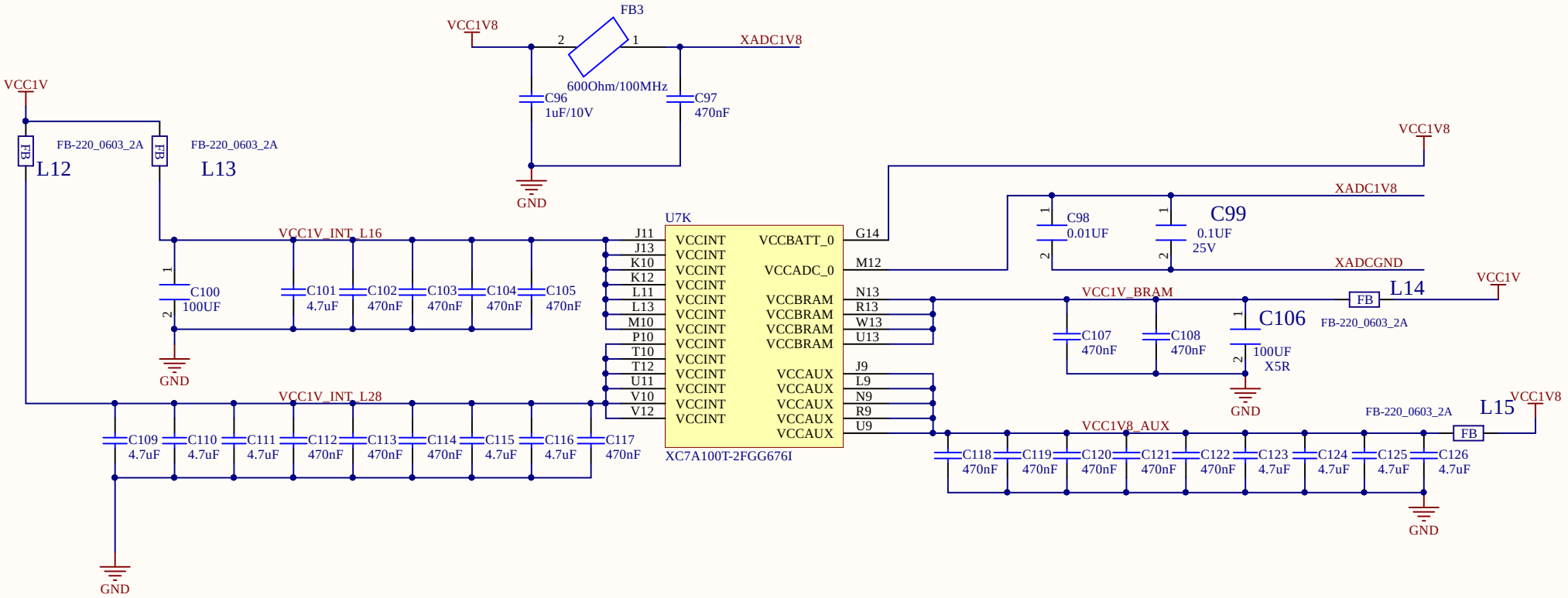

BANK15 3.3V
BANK16 3.3V
U7C
BANK 15
XC7A100T-2FGG676I
U7D
BANK 16
XC7A100T-2FGG676I
U8
TFT_LCD(320x480)
Company: FII
Title: FPGA_BANK15_16 of PRX100T
Date: 2019-07-20 Time 3:33:05 PM
Engineer: RJ WG
Revision: V3
Sheet 09 of 23
Size: B+



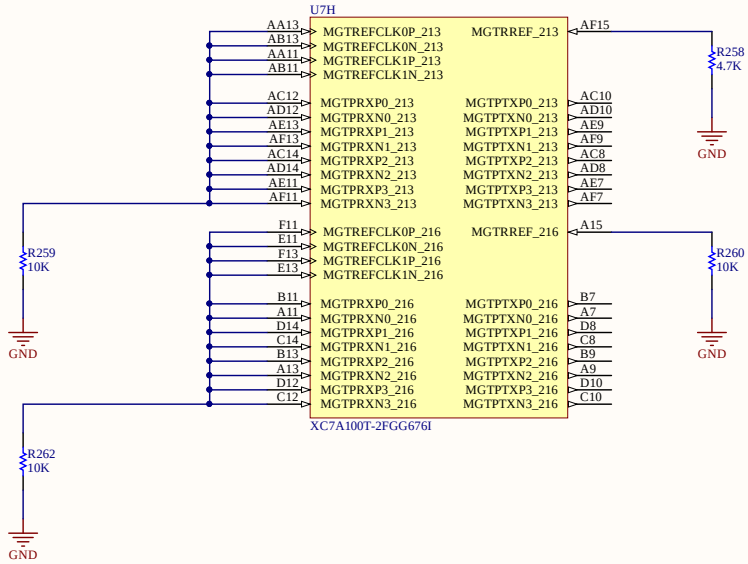
Company: FII		Title: FPGA_BANK34_35 of PRX100T	
Date: 2019-07-20	Time 3:33:05 PM	Engineer: RJ WG	Size:
Revision: V3		Sheet 10 of 23	B+

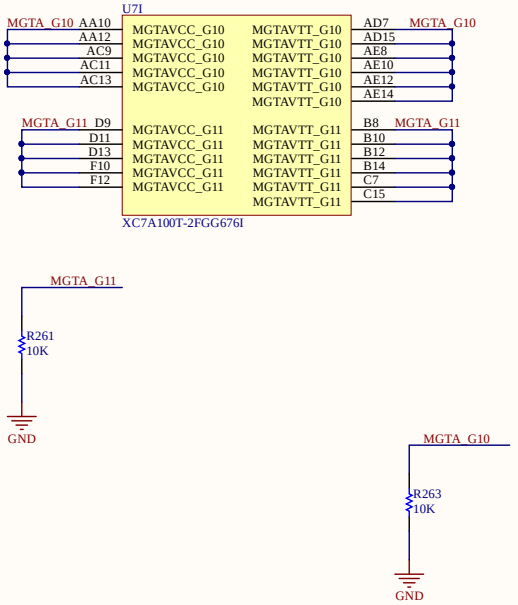


Company: FII		Title: FPGA_IO_POWER of PRX100T	
Date: 2019-07-20	Time 3:33:05 PM	Engineer: RJ WG	Size:
Revision: V3		Sheet 11 of 23	B+

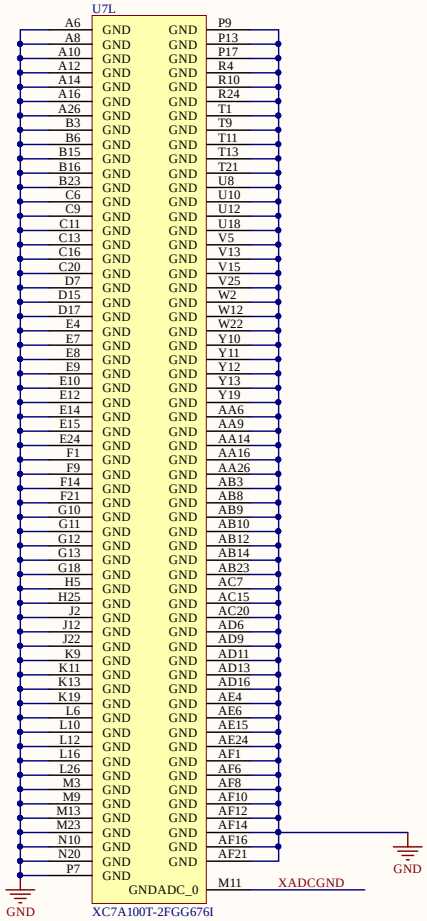


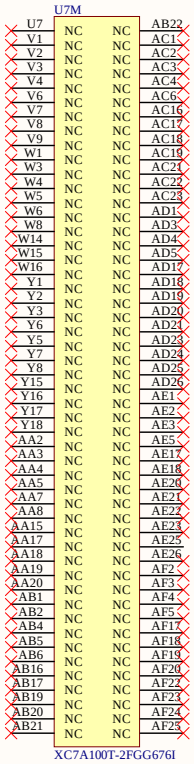
Company: FII	Title: FPGA_CORE_POWER of PRX100T		
Date: 2019-07-20 Time 3:33:05 PM		Engineer: RJ WG	Size: A4
Revision: V3		Sheet 12 of 23	



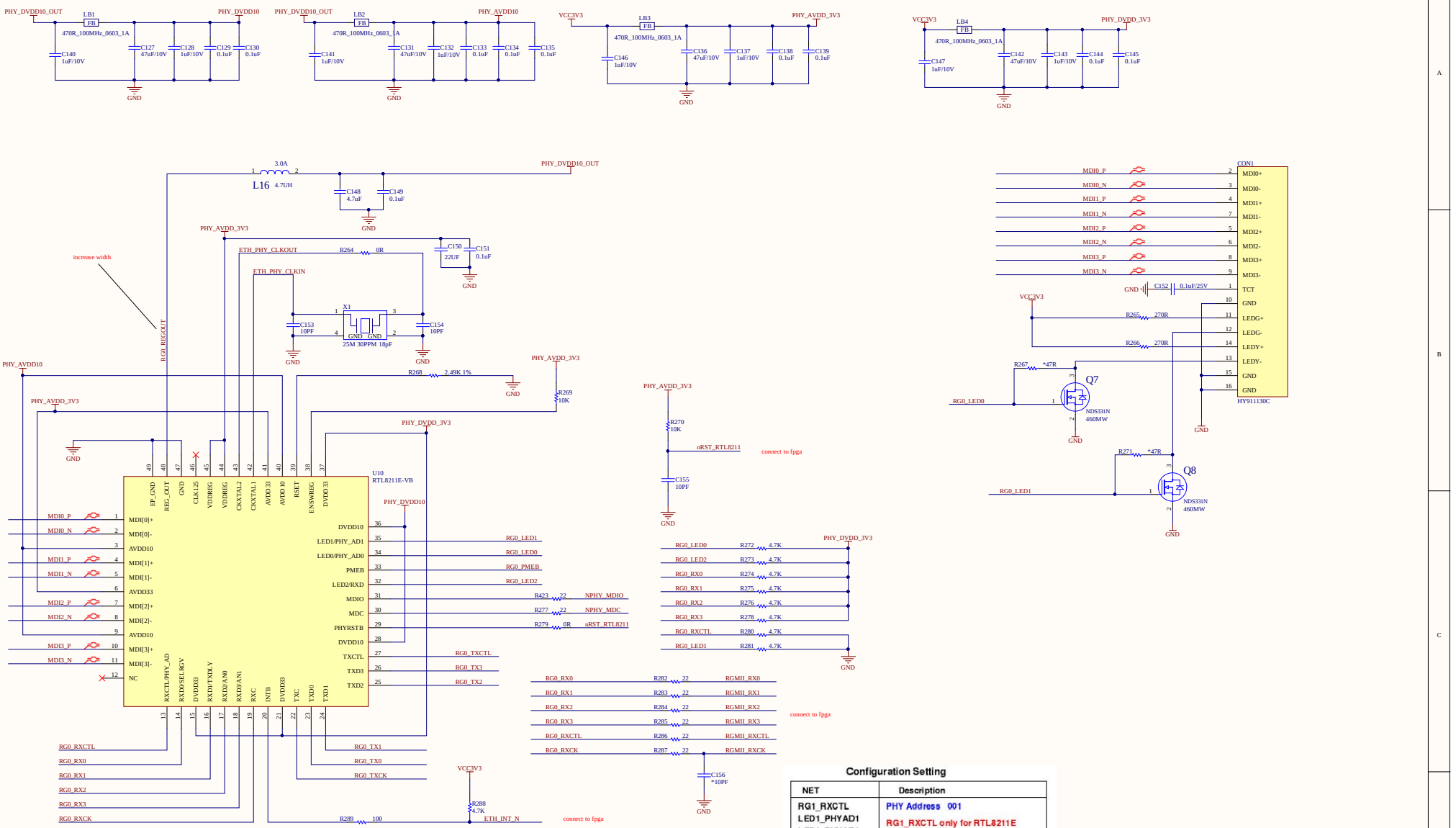


Company: FII	Title: FPGA_MGT of PRX100T		
Date: 2019-07-20 Time 3:33:05 PM		Engineer: RJ WG	Size: B
Revision: V3	Sheet 13 of 23		





Company: FII	Title: FPGA_GND_NC of PRX100T		
Date: 2019-07-20 Time 3:33:05 PM		Engineer: RJ WG	Size: B
Revision: V3	Sheet 14 of 23		



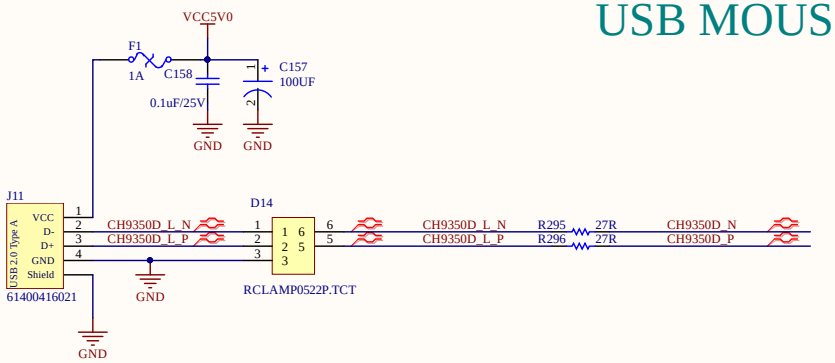
ETHERNET

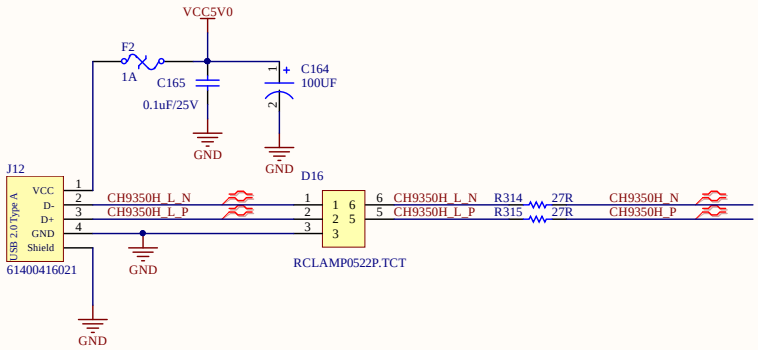
Configuration Setting

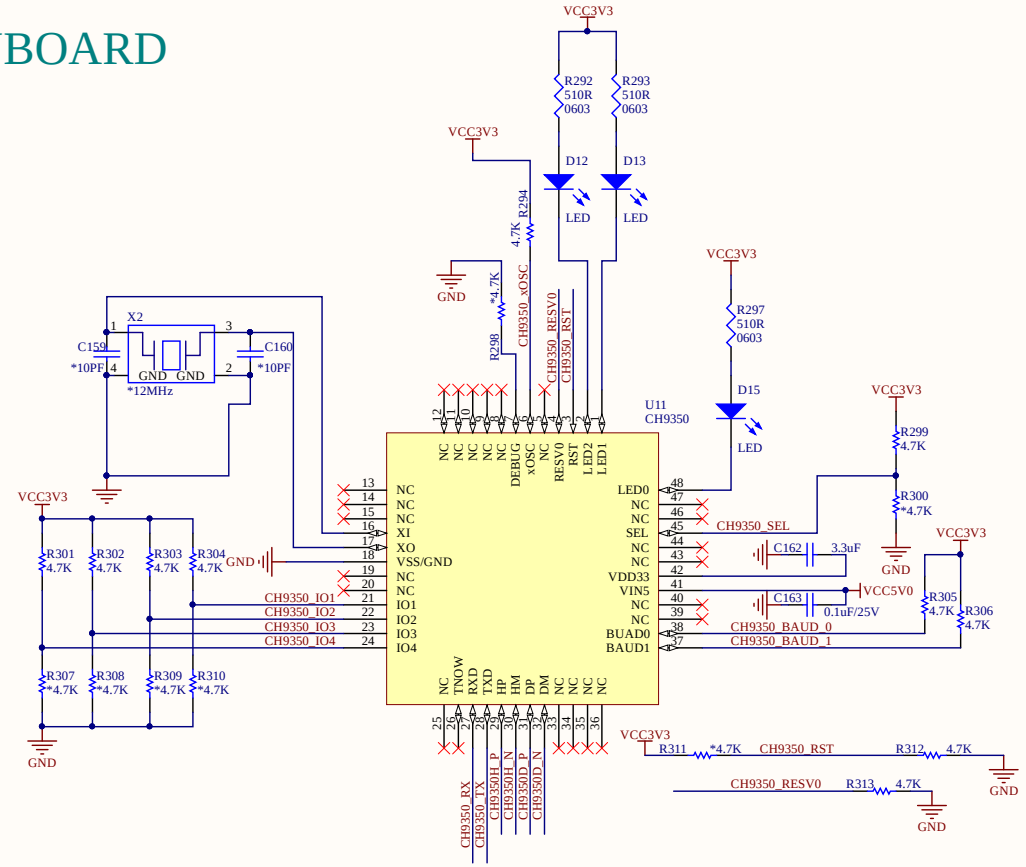
NET	Description
RG1_RXCTL LED1_PHYAD1 LED0_PHYAD0	PHY Address 001 RG1_RXCTL only for RTL8211E
LED2_RXDLY RG1_RX1	High ==>TX/RX Delay Low ==>Without TX/RX Delay
RG1_RX0/SELRGV	Pull up for 3.3v RGMII
RG1_RX2/AN0 RG1_RX3/AN1	High ==>Config for all capability

Company: FII	Title: ETHERNET	of PRX100T	
Date: 2019-07-20	Time 3:33:06 PM	Engineer: RJ WG	Size:
Revision: V3	Sheet 15 of 23		C

USB MOUSE/KEYBOARD



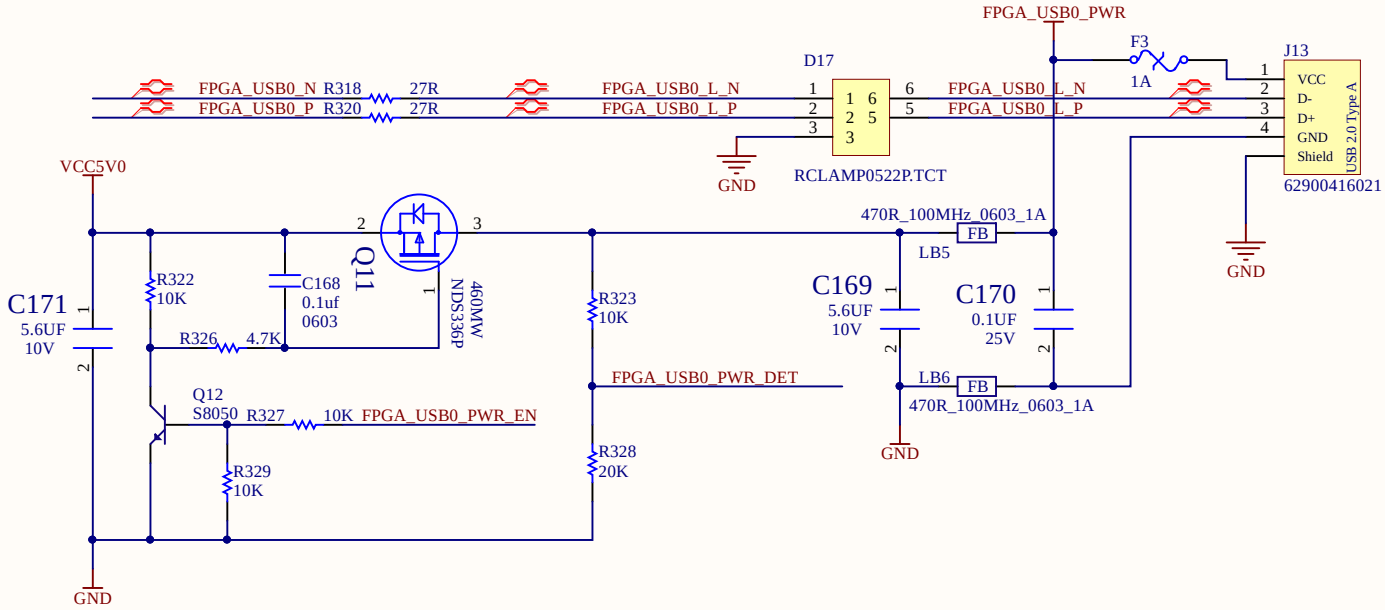


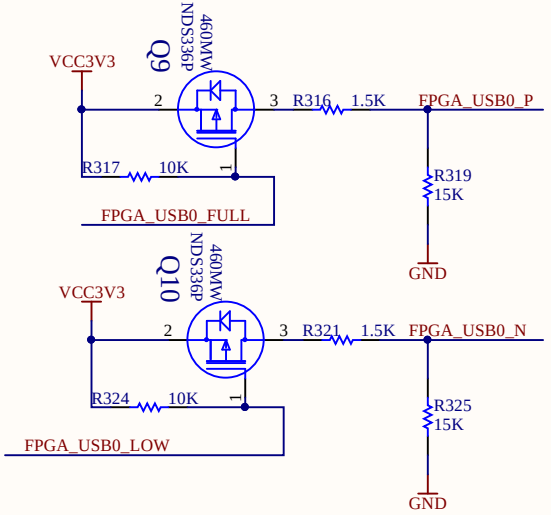


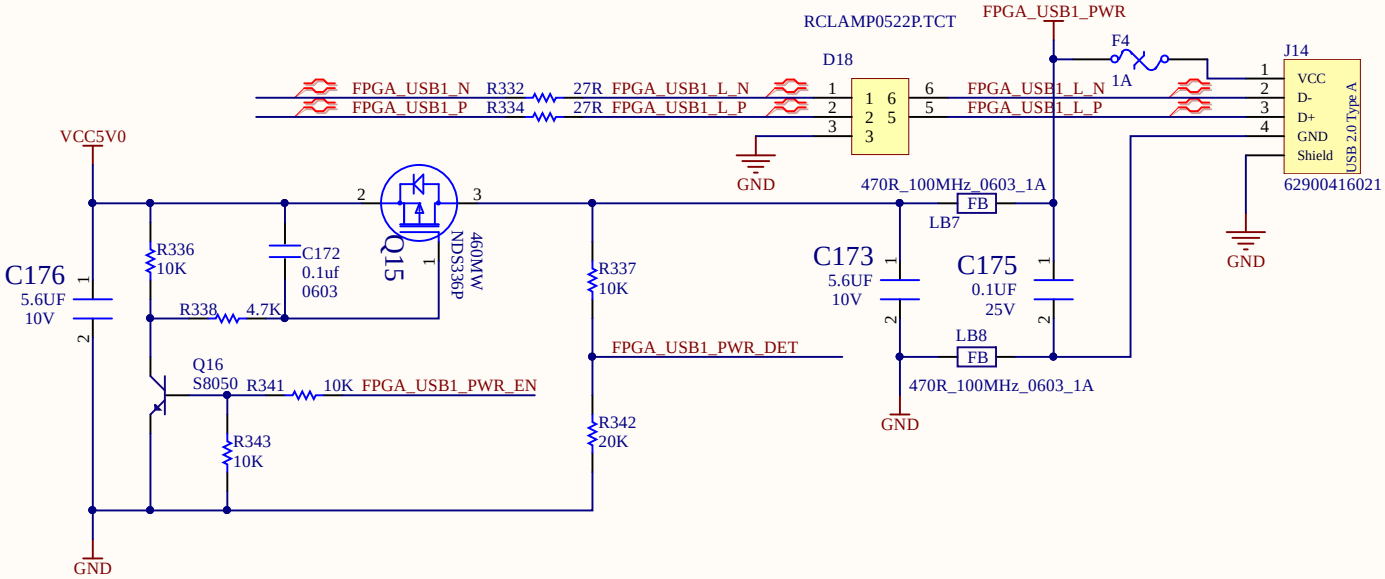
USB DEVICE TO UART

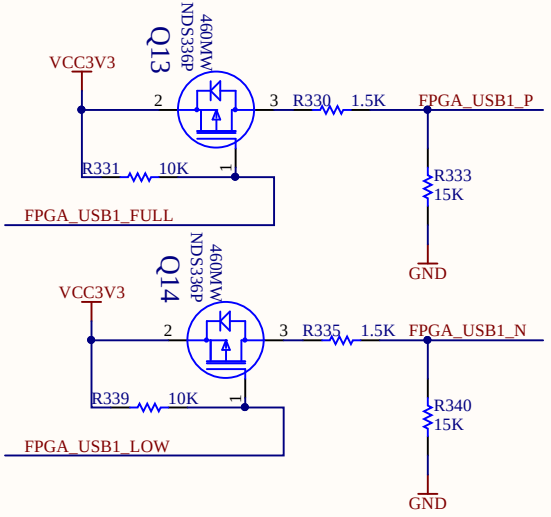
Company: FII	Title: USB	of PRX100T	
Date: 2019-07-20	Time 3:33:06 PM	Engineer: RJ WG	Size: B
Revision: V3	Sheet 16 of 23		

USB PHY



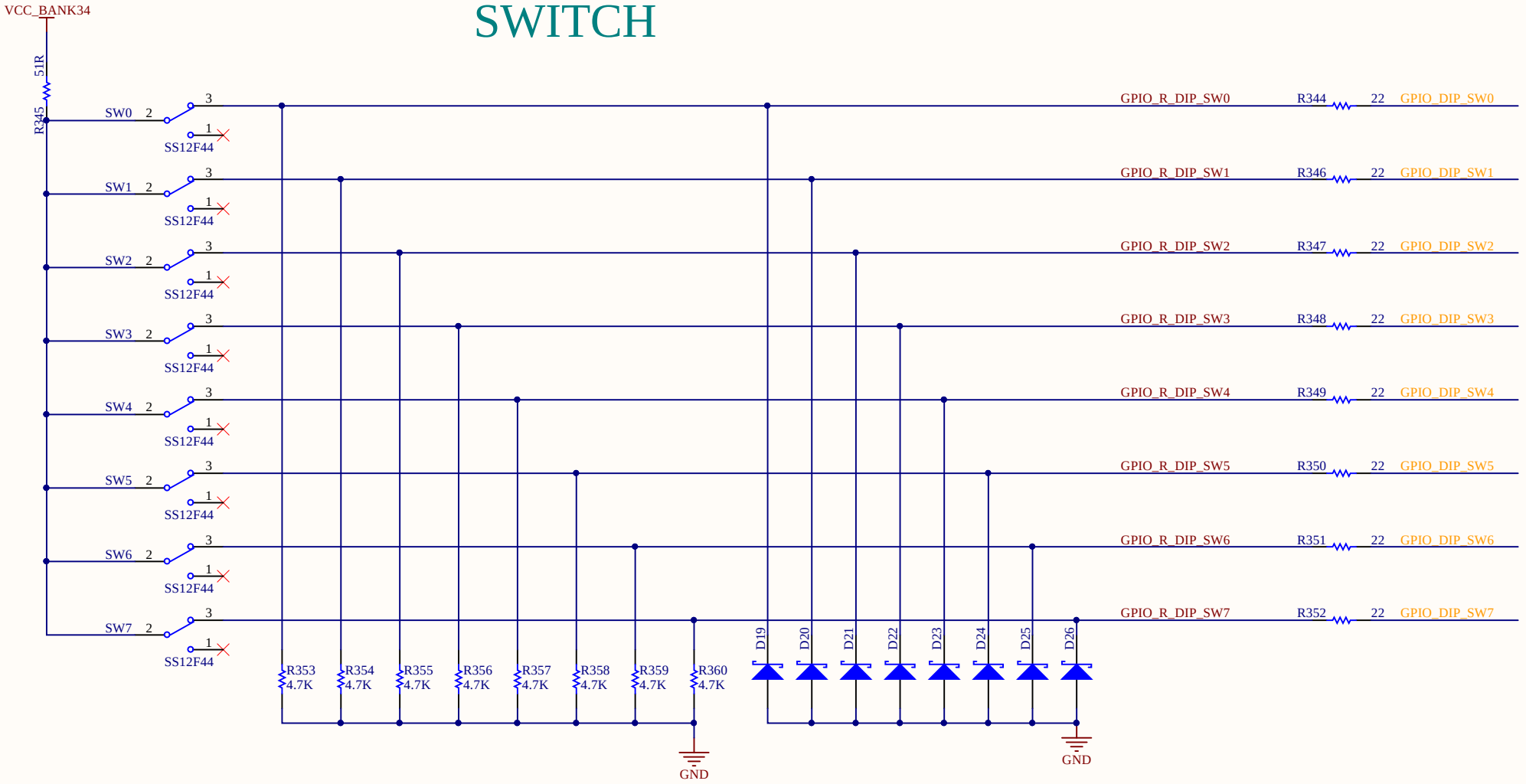






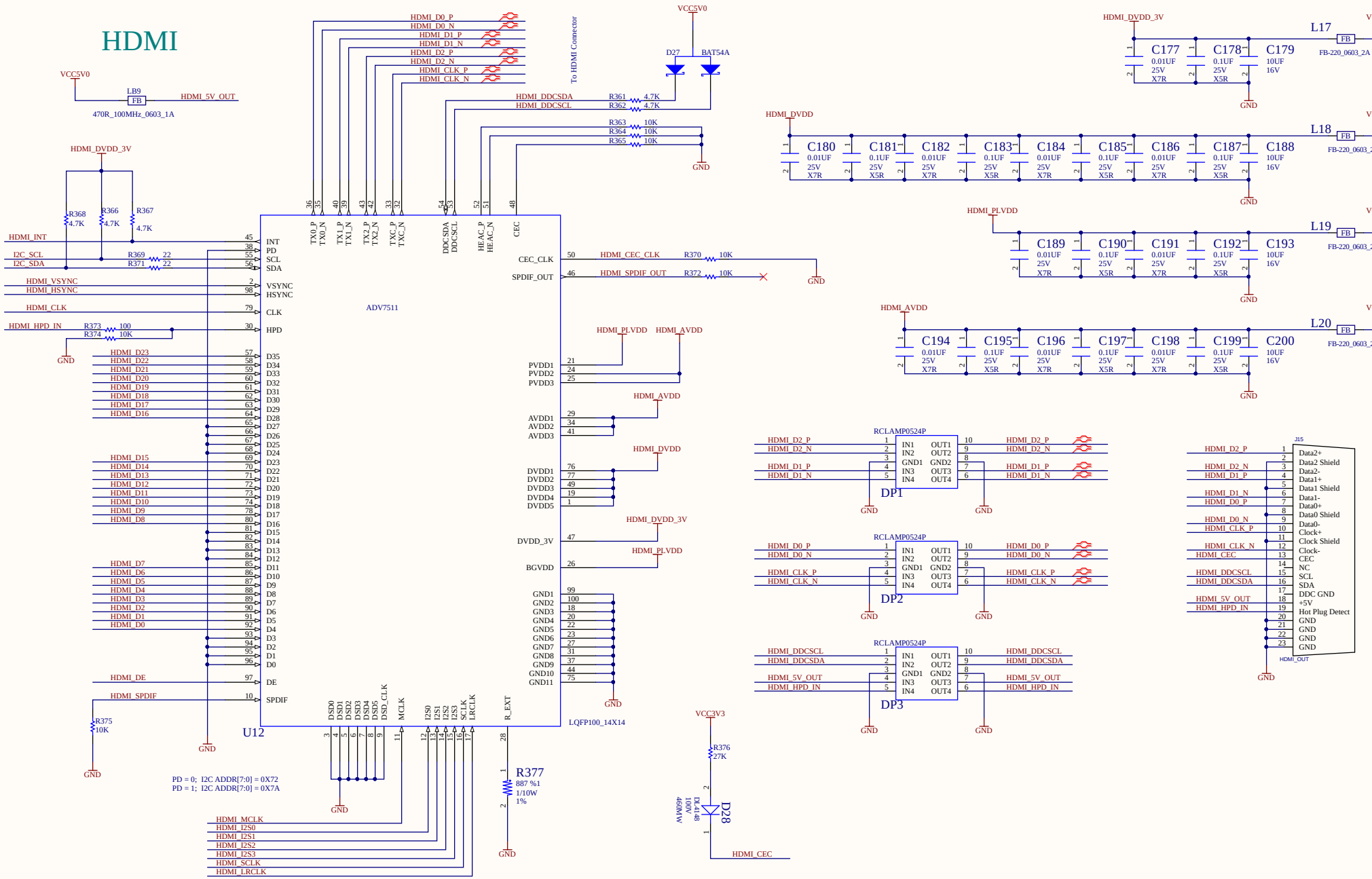
Company: FII	Title: USB_FPGA of PRX100T		
Date: 2019-07-20 Time 3:33:06 PM		Engineer: RJ WG	Size: A4
Revision: V3		Sheet 17 of 23	

SWITCH



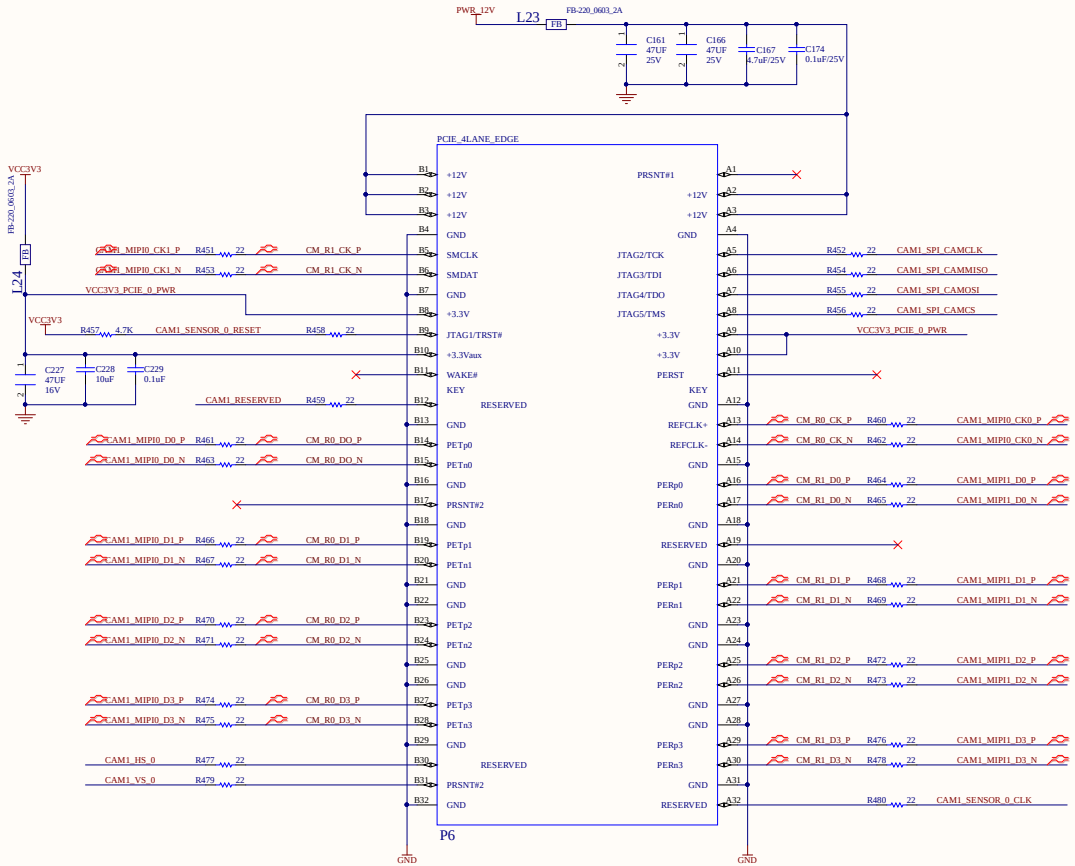
Company: FII	Title: SW of PRX100T		
Date: 2019-07-20 Time 3:33:06 PM		Engineer: RJ WG	Size: A4
Revision: V3	Sheet 18 of 23		

HDMI



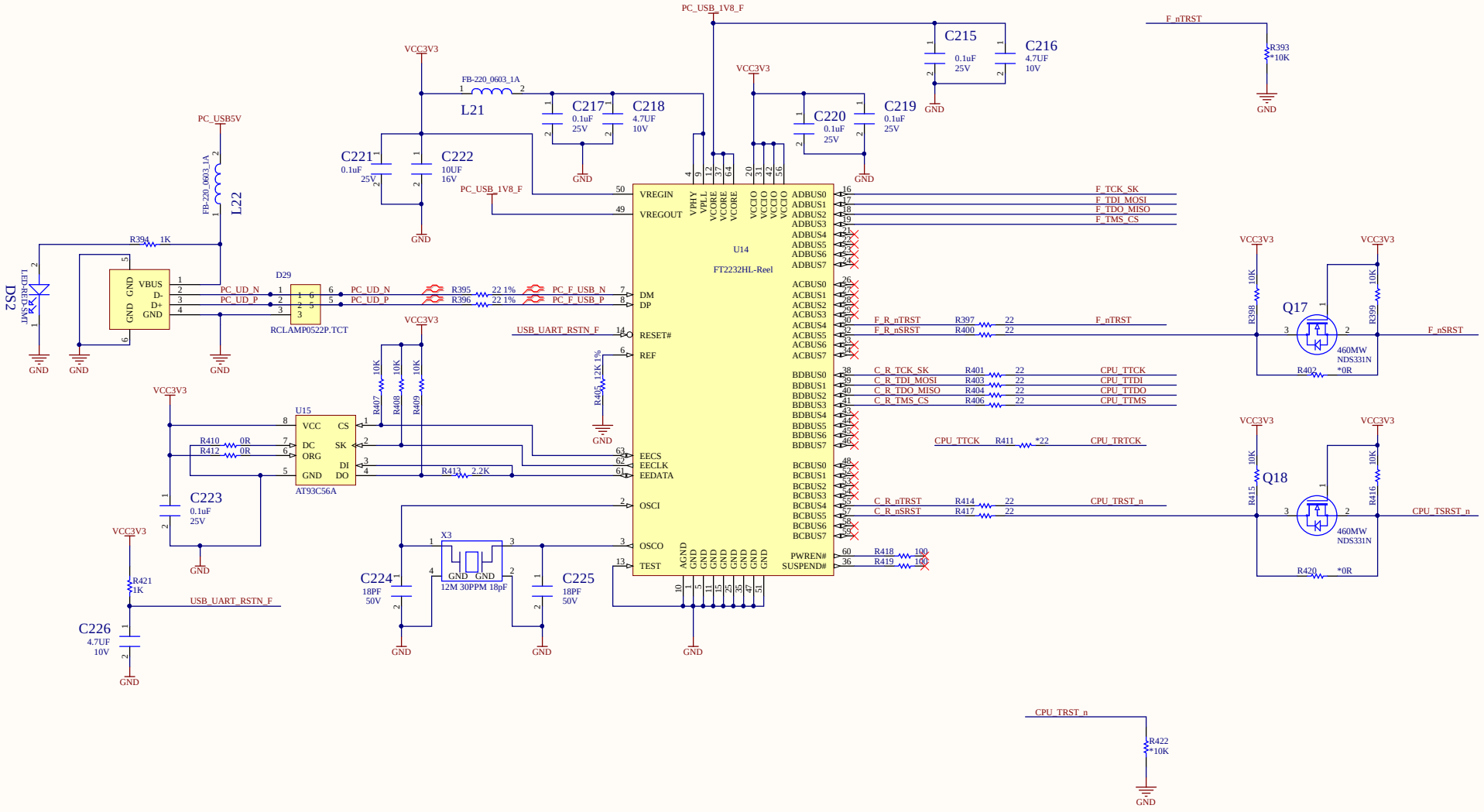
Company: FII		Title: HDMI	of PRX100T
Date: 2019-07-20	Time 3:33:06 PM	Engineer: RJ WG	Size: B+
Revision: V3		Sheet 19 of 23	

PCIE



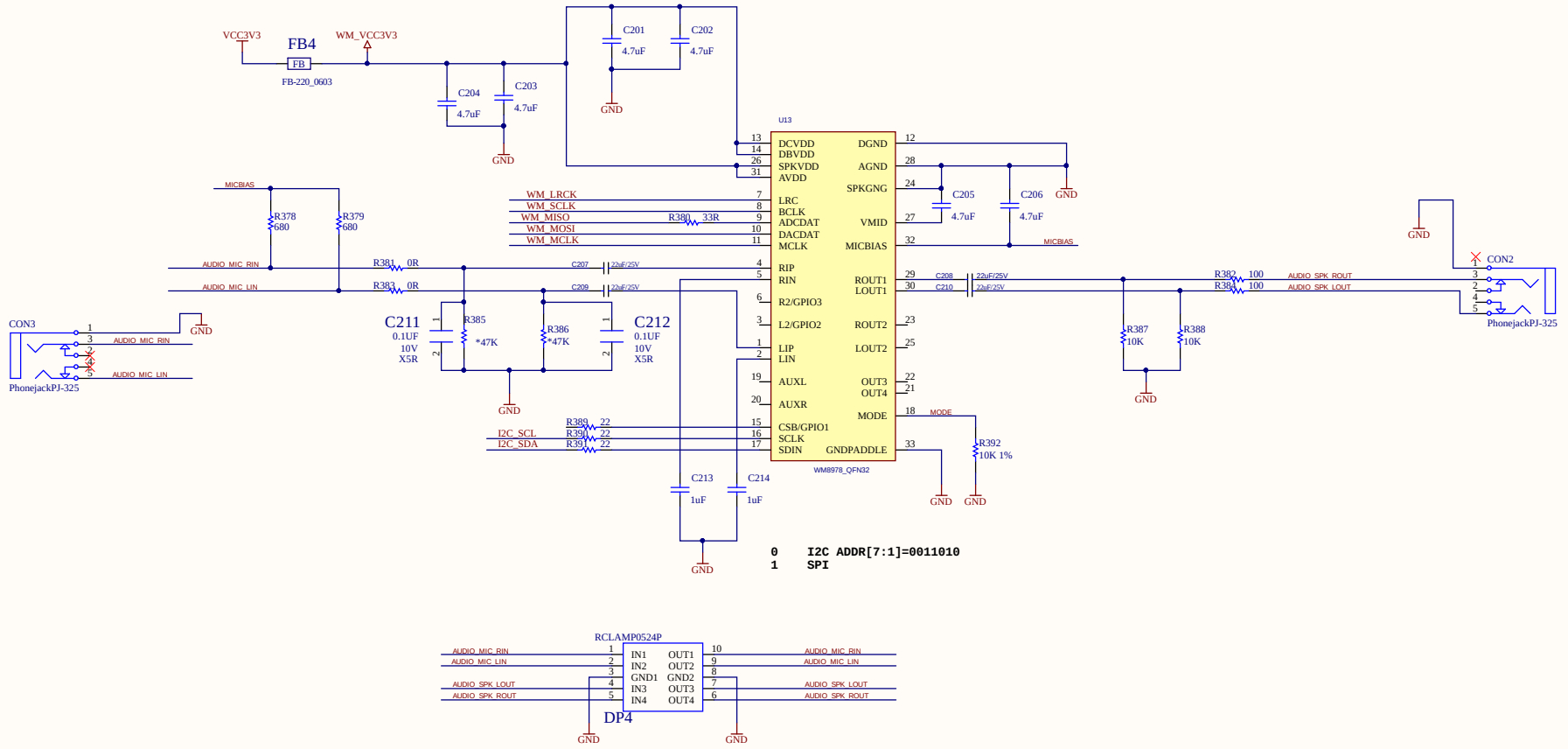
Company: FII	Title: PCIE		of PRX100T
Date: 2019-07-20	Time 3:33:07 PM	Engineer: RJ WG	Size: C
Revision: V3		Sheet 20 of 23	

JTAG/UART



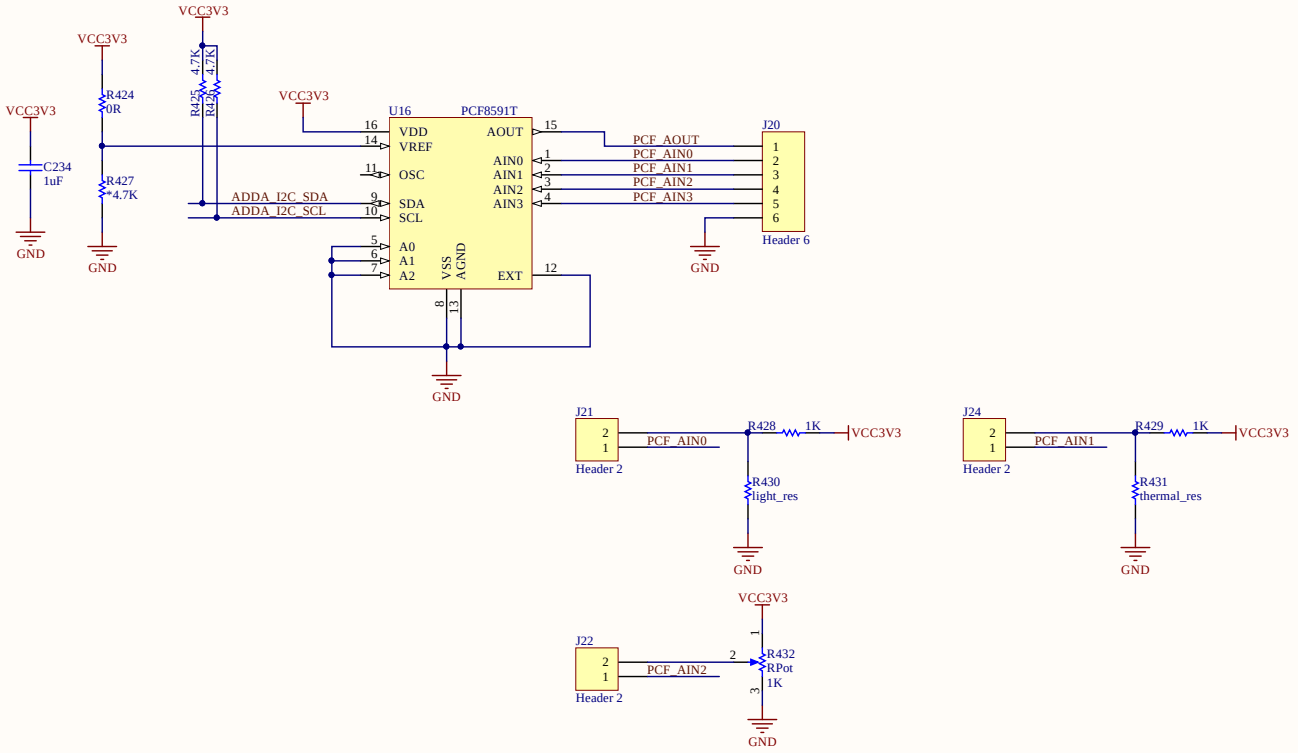
Company: FII	Title: USB_2_UART of PRX100T		
Date: 2019-07-20 Time 3:33:07 PM	Engineer: RJ WG	Size:	
Revision: V3	Sheet 21 of 23	B+	

AUDIO WM8978



Company: FII		Title: AUDIO of PRX100T	
Date: 2019-07-20	Time 3:33:07 PM	Engineer: RJ WG	Size:
Revision: V3		Sheet 22 of 23	B+

AD/DA



Company: FII	Title: AD_DA of PRX100T		
Date: 2019-07-20 Time 3:33:07 PM	Engineer: RJ WG	Size: B	
Revision: V3	Sheet 23 of 23		